

**Fig. 1**

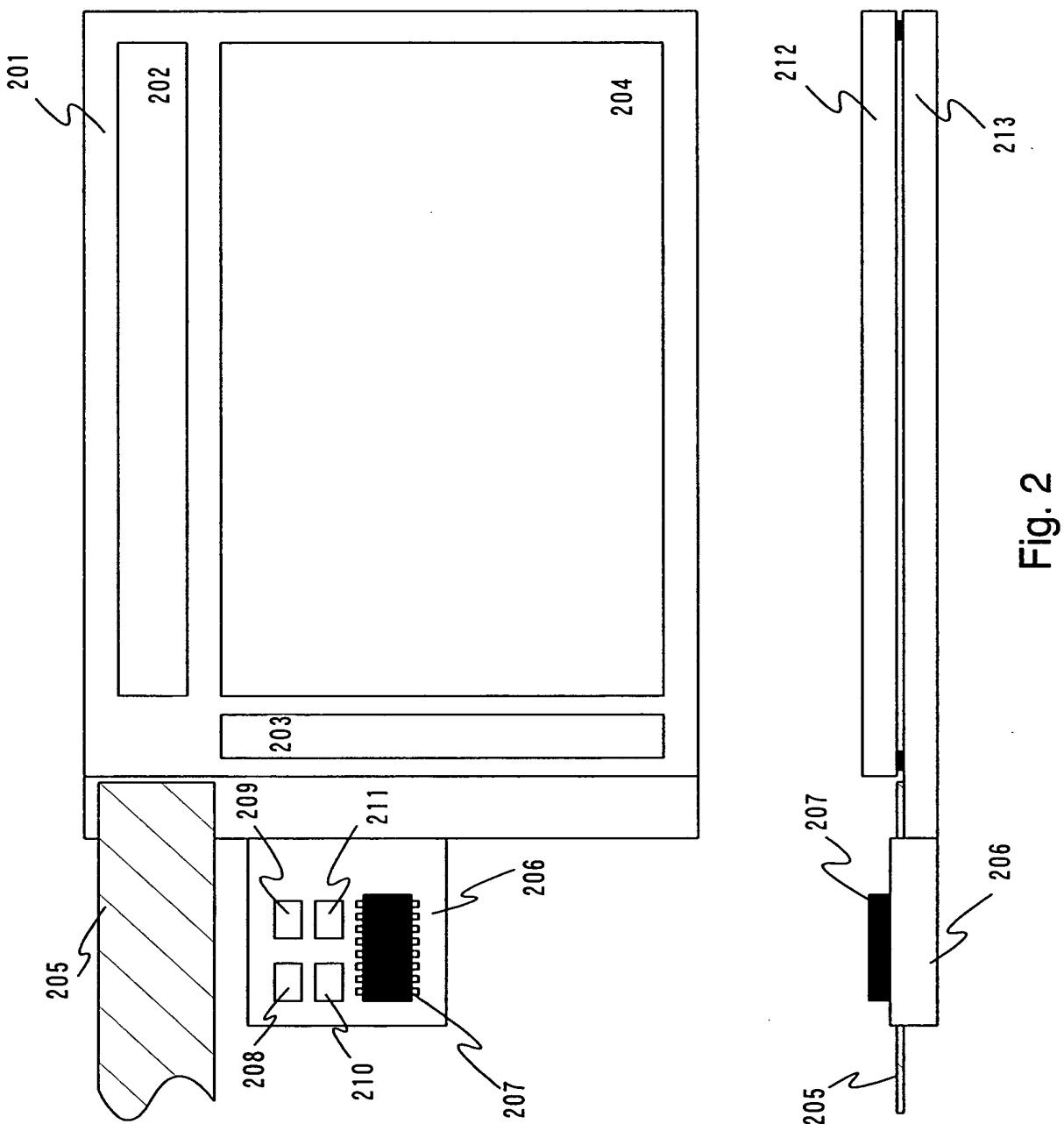
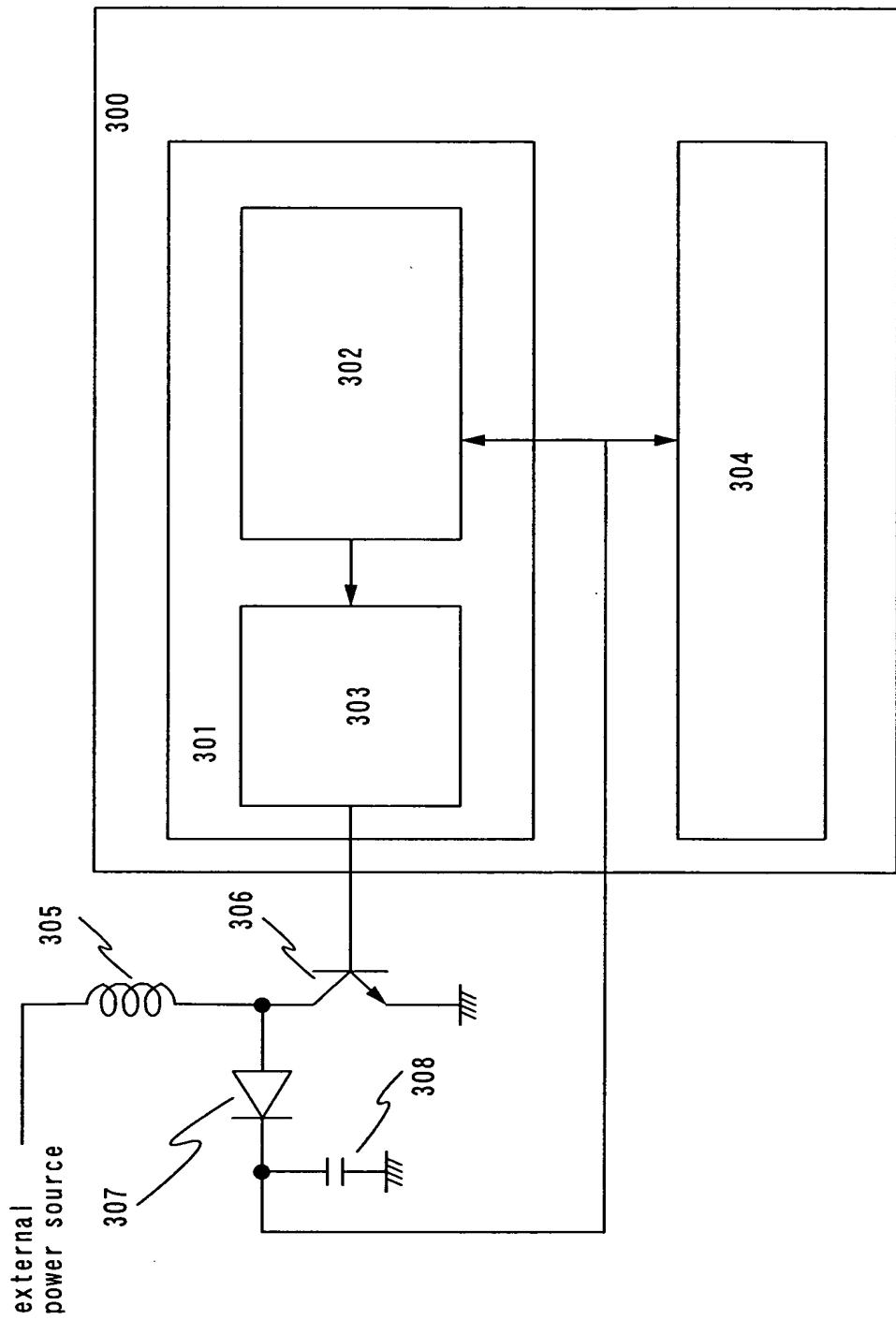
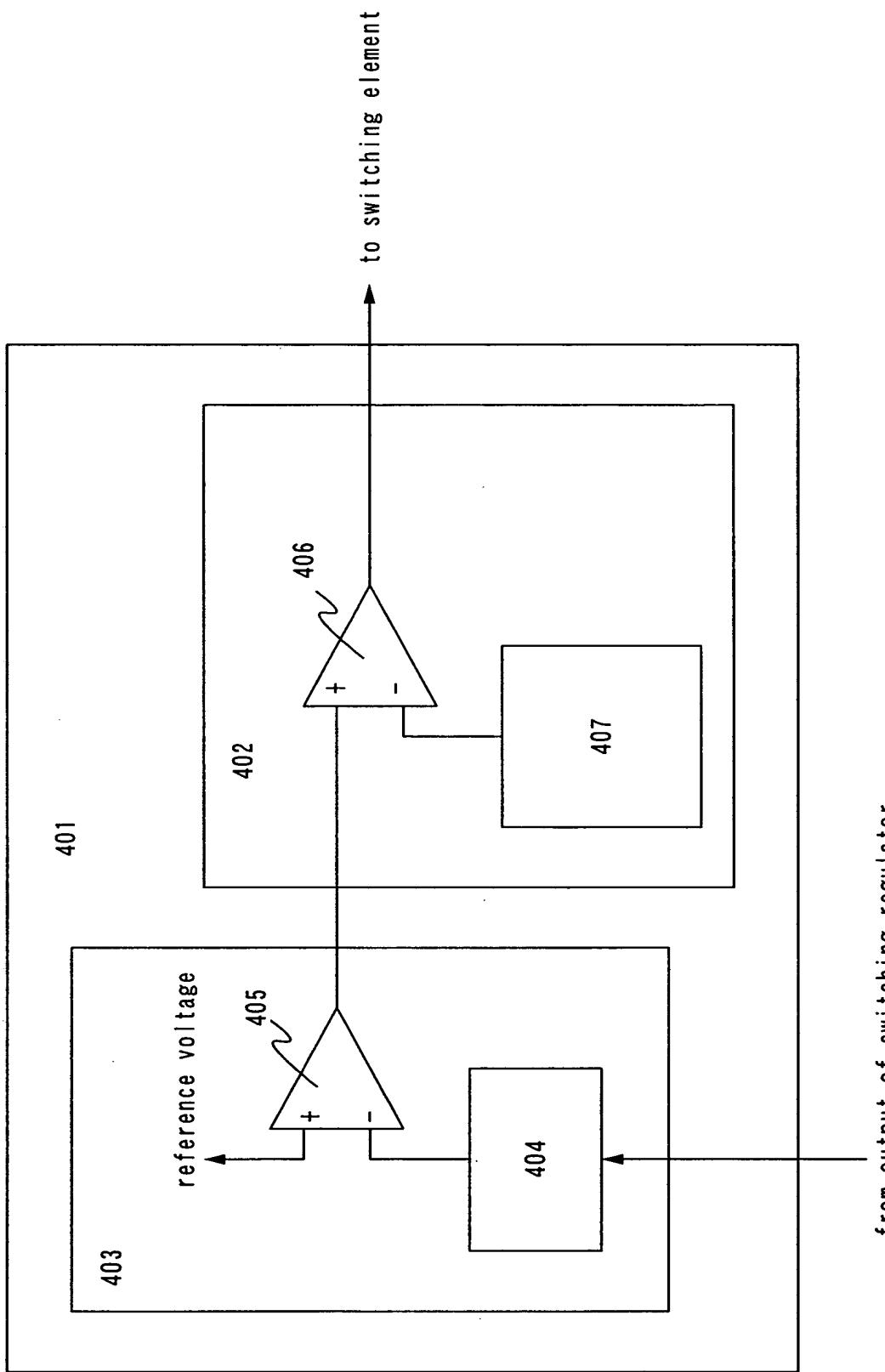


Fig. 2





from output of switching regulator

Fig. 4

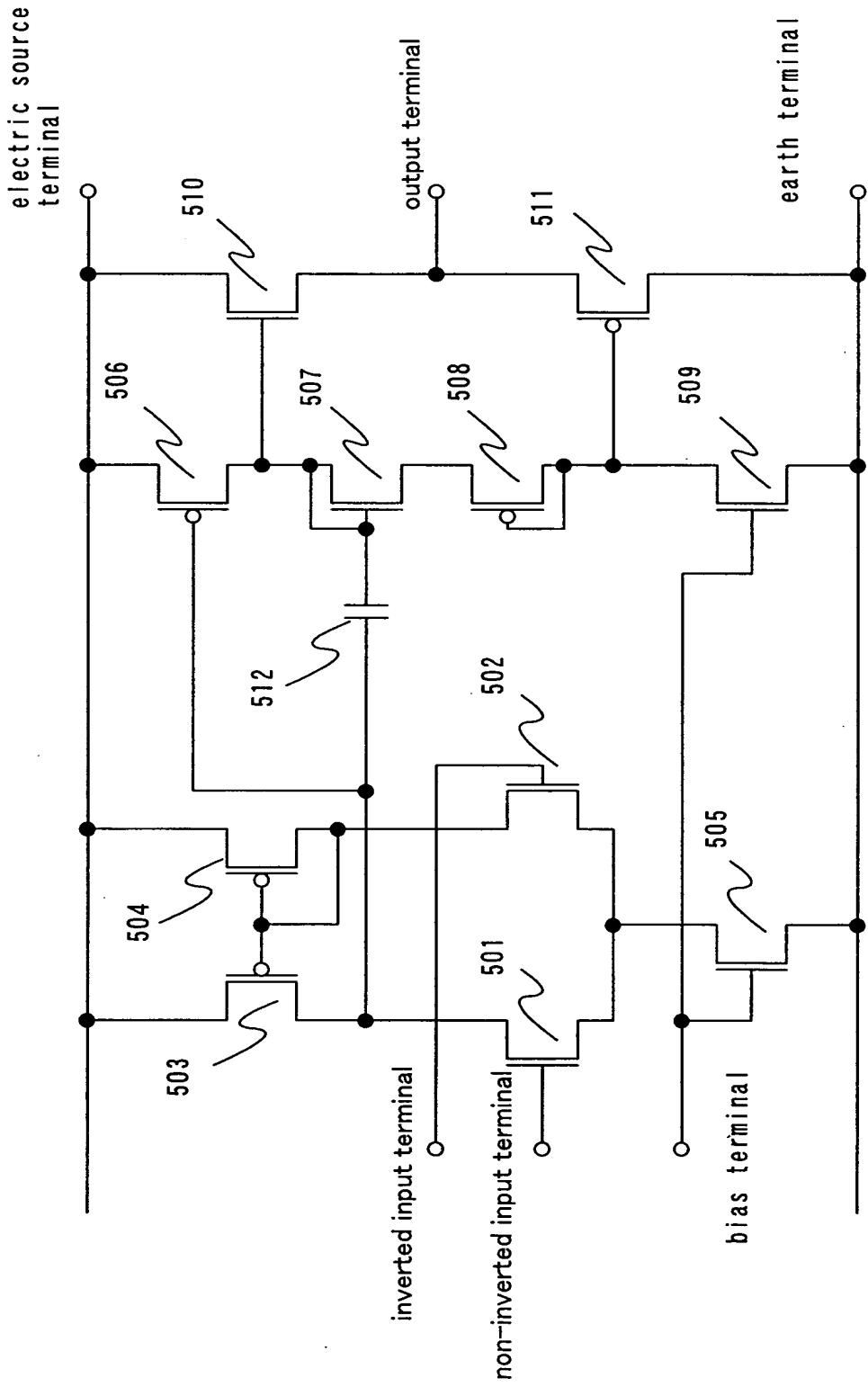
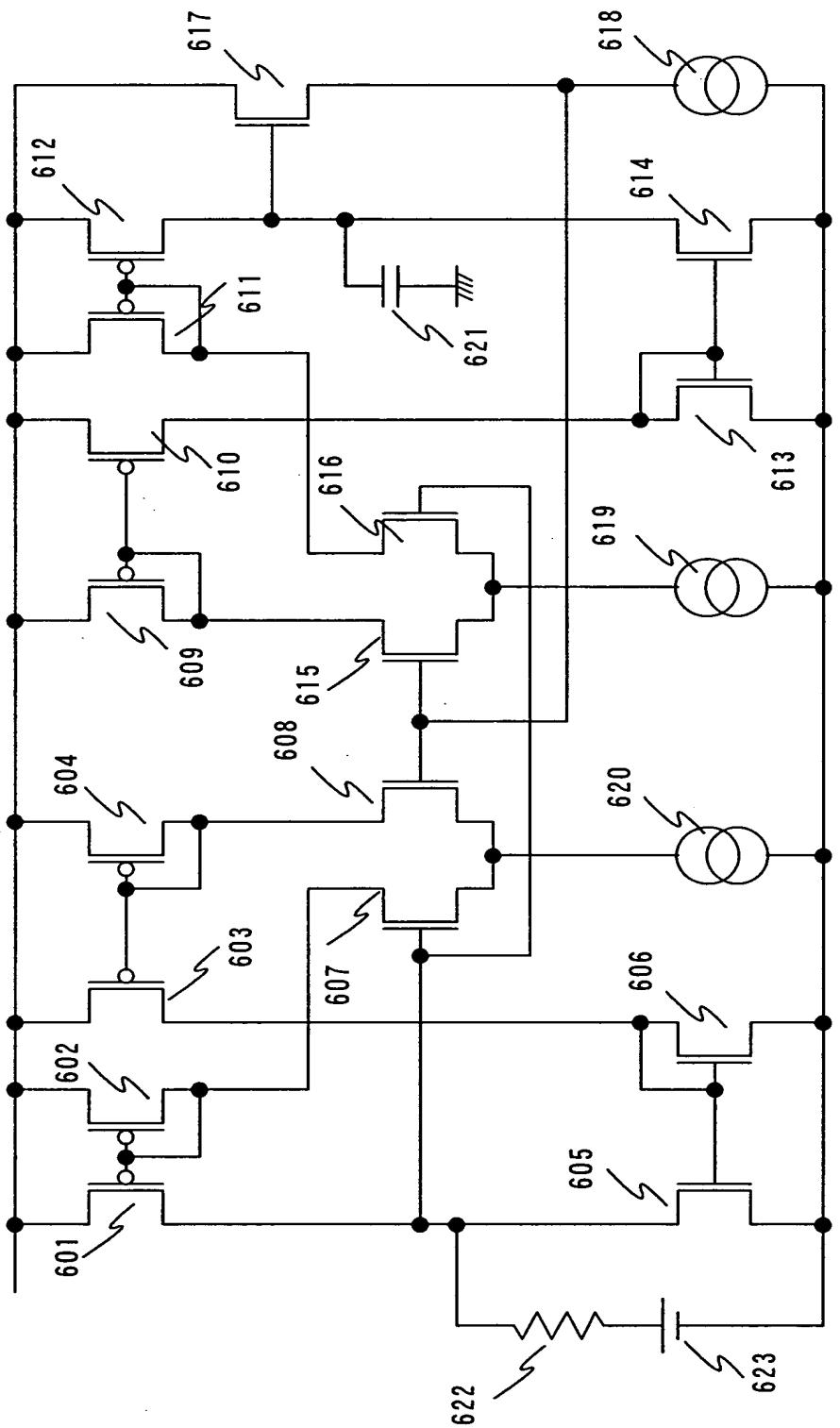


Fig. 5

Fig. 6



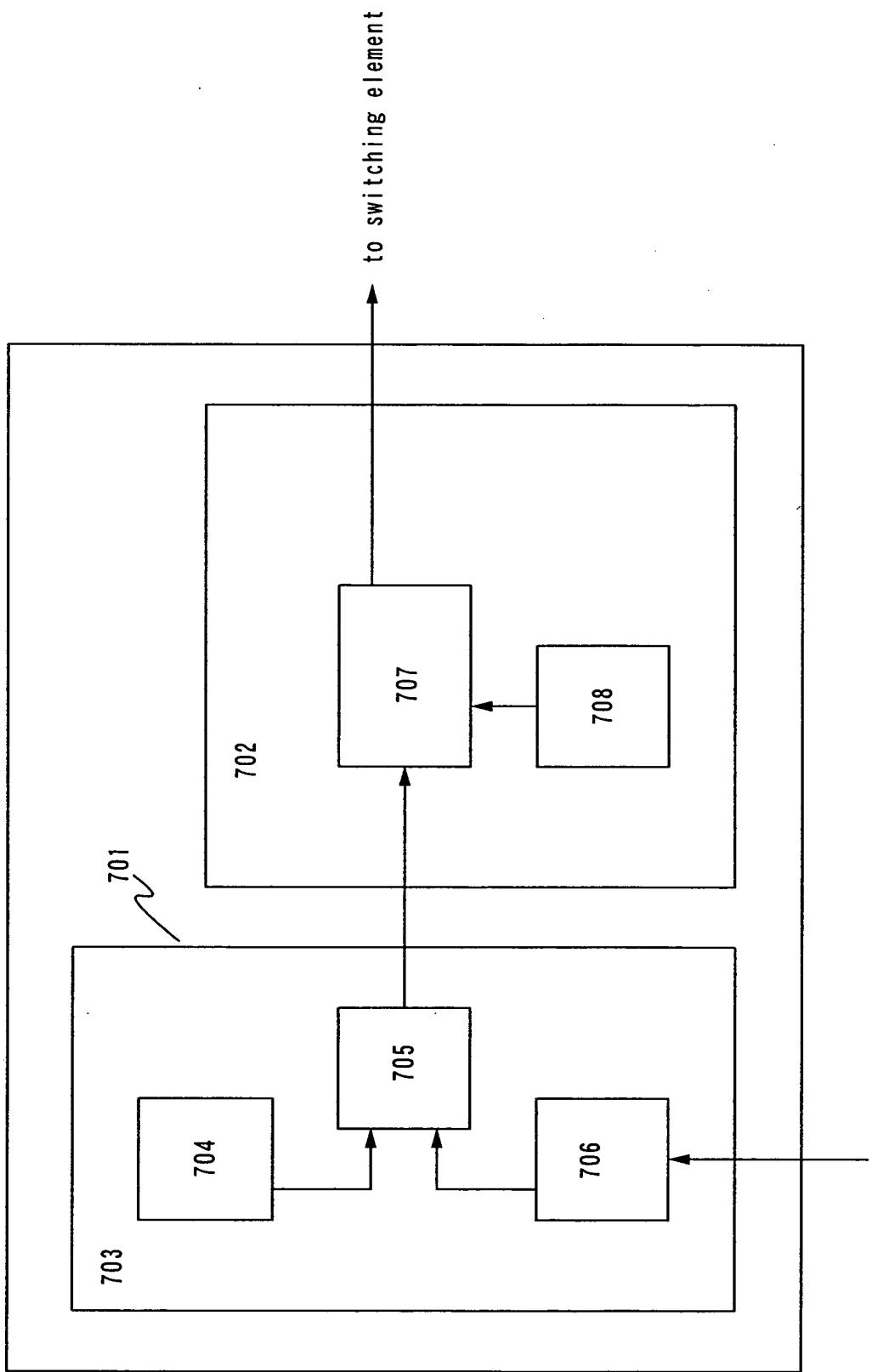


Fig. 7

from output of switching regulator

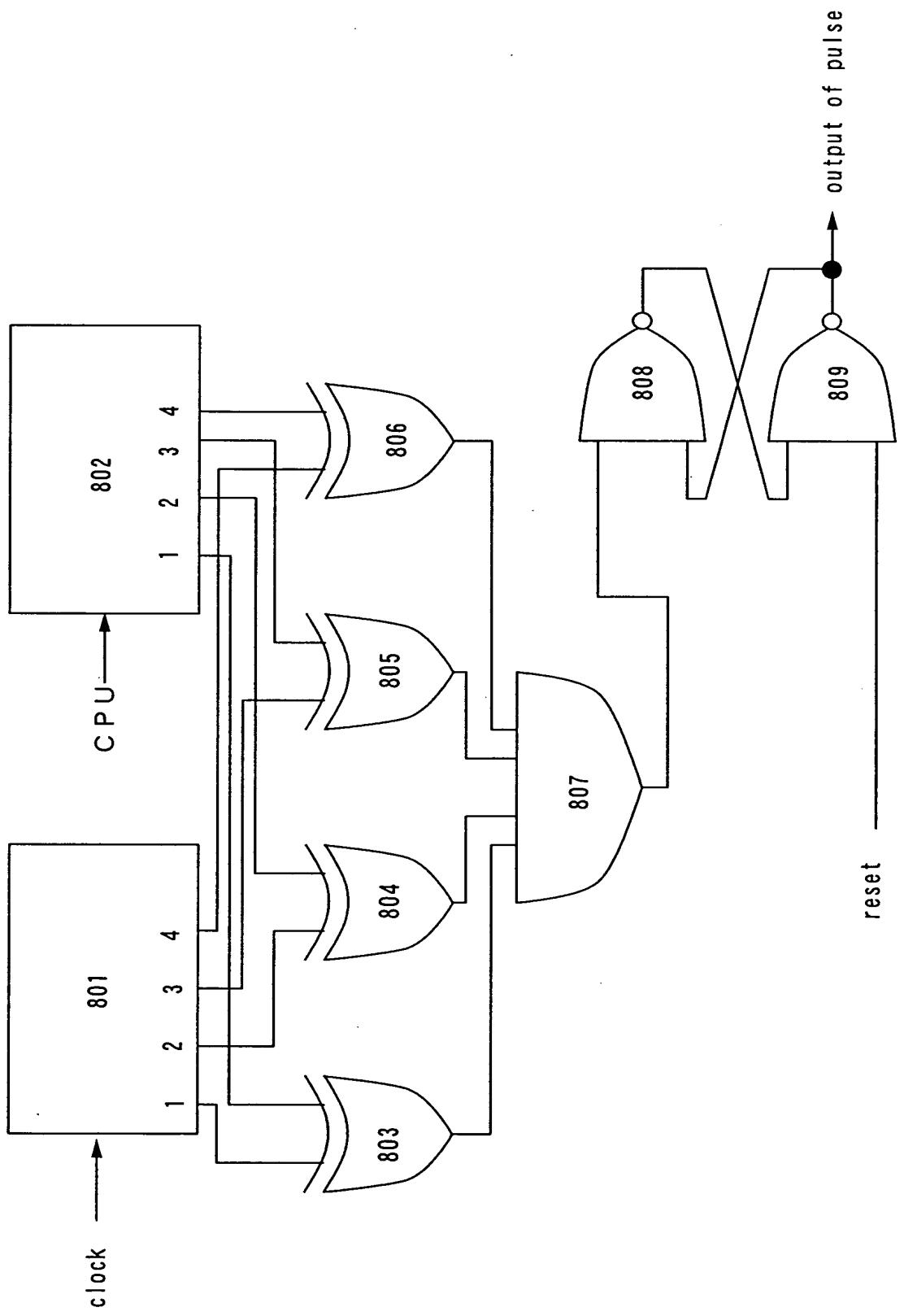


Fig. 8

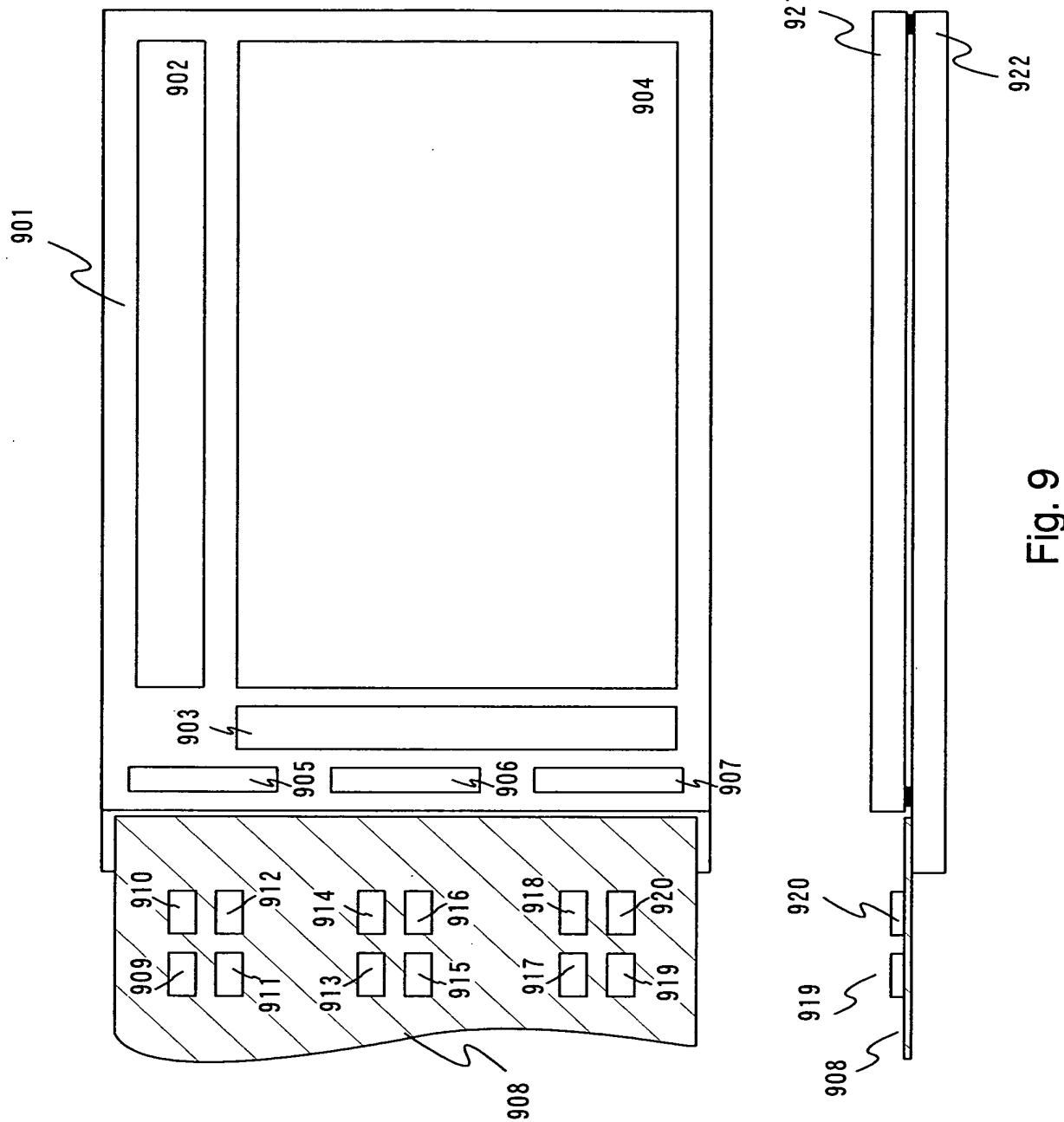


Fig. 9

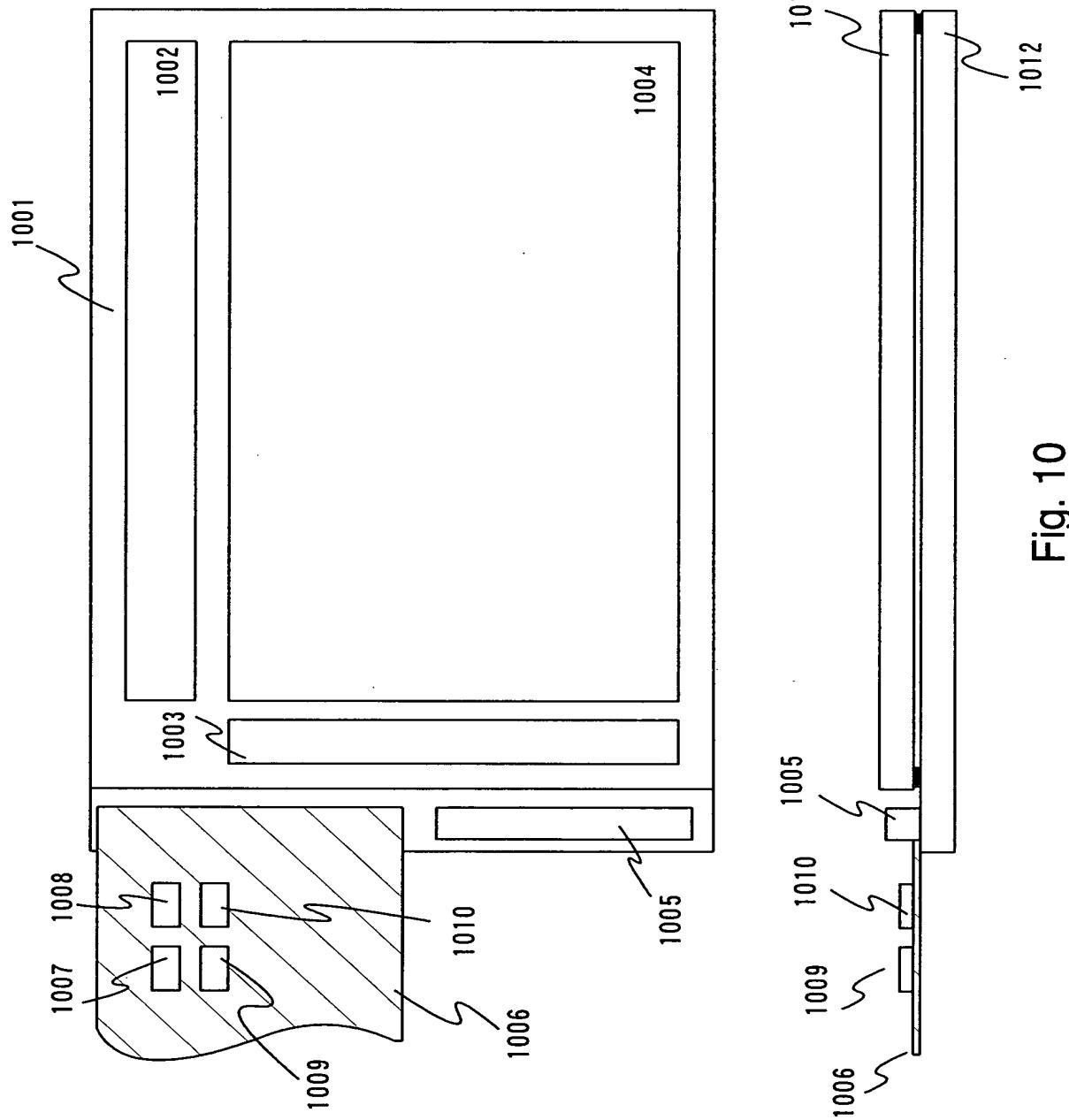


Fig. 10

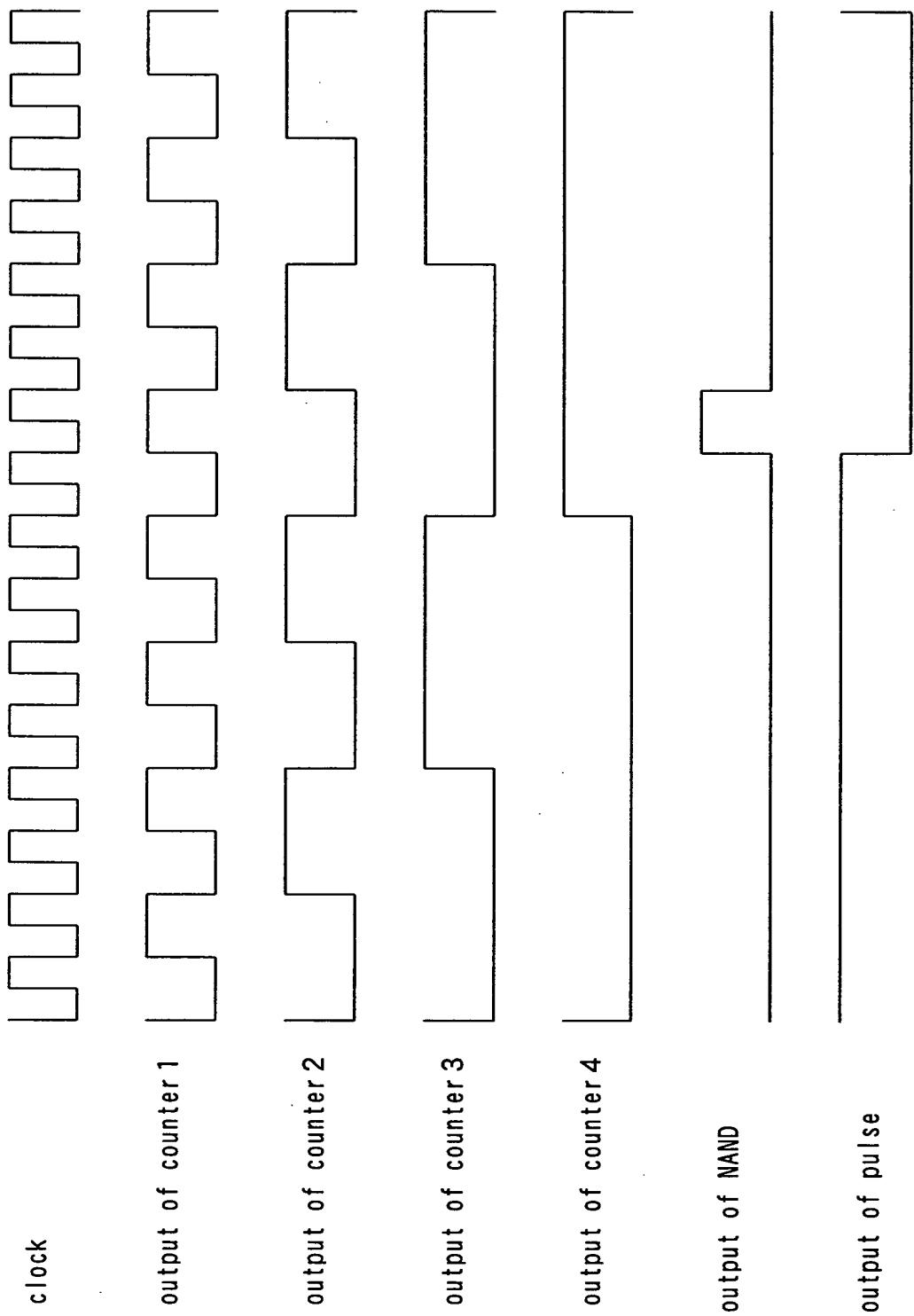


Fig. 11

